



SRINIVASA INSTITUTE OF TECHNOLOGY & SCIENCE
(AUTONOMOUS)

Chennai-Hyderabad Bypass Road, Ukkayapalli, Kadapa, A.P-516002



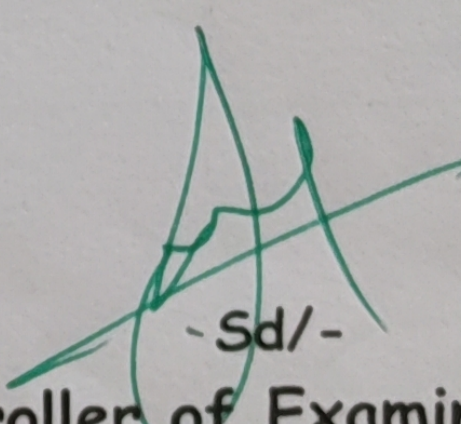
Examinations Branch

M.Tech II Semester (SITS24) Supplementary Examinations-August-2026

Time: 10:00AM to 01:00PM

Date/Day	ELECTRICAL POWER SYSTEMS	VLSI SYSTEM DESIGN	COMPUTER SCIENCE & ENGINEERING
14.08.2026 (Friday)	Power System Stability and Control 24MEP201	CMOS Mixed Signal IC Design 24MVD201	Advanced Operating Systems 24MCS201
17.08.2026 (Monday)	FACTS Controllers 24MEP202	Physical Design Automation 24MVD202	Internet of Things 24MCS202
19.08.2026 (Wednesday)	Reactive power Compensation & Management 24MEP203c	SoC Testing and Verification 24MVD203a	Deep Learning 24MCS203a
22.08.2026 (Saturday)	Distributed Generation and Micro grid Control 24MEP204b	Low Power VLSI Design 24MVD204a	Distributed Systems 24MCS204b

Notification Date: 21.07.2026


- Sd/-
Controller of Examinations
Controller of Examination